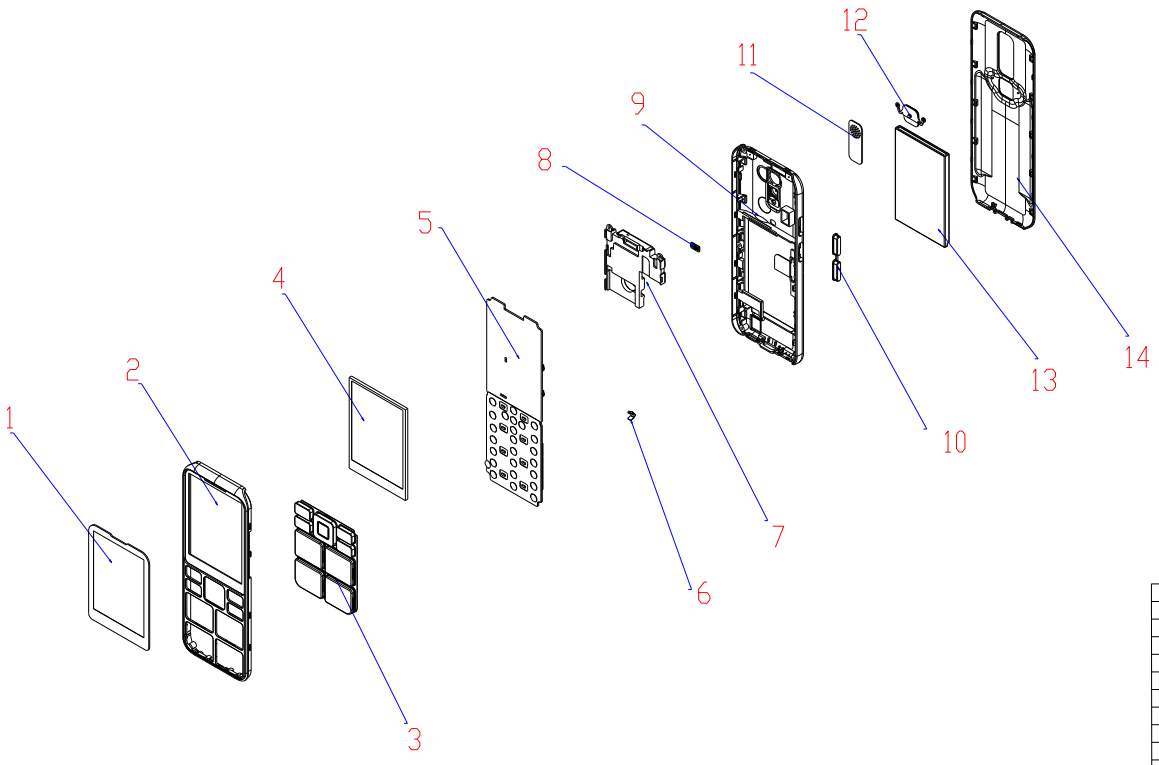

14		C shell	1	
13		Battery	1	
12		key	1	
11		Lens	1	
10		key	1	
9		B shell	1	
8		Lens	1	
7		Loudspeaker	1	
6		Shrapnel	1	
5		Mainboard	1	
4		display	1	
3		key	1	
2		A shell	1	
1		lens	1	
S/N	CP	PN	Qty	RS

CL8000

CL8000 Service Part List

Item	Description	Unit	Price
1	CL8000 E23 Battery	1	Please contact our helpline
2	CL8000 rear-facing camera assembly	1	Please contact our helpline
3	CL8000 external charging port(s)	1	Please contact our helpline
4	CL8000 mechanical button(s)	1	Please contact our helpline
5	CL8000 main microphone(s)	1	Please contact our helpline
6	CL8000 speaker(s)	1	Please contact our helpline
7	CL8000 mechanical display folding mechanism	1	Please contact our helpline

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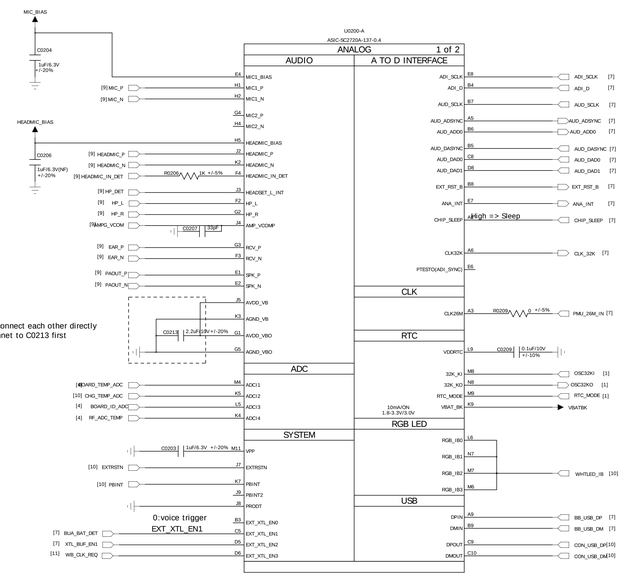
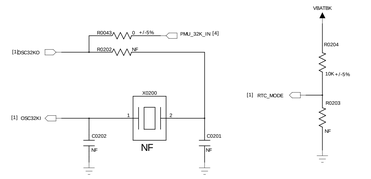
PMU_ANALOG



BKBT

RTC



RTC Mode	R0204	R0203	X0200	C0201	C0202	R0202	R0214	
32K Crystal	NF	0R	Crystal	8.2pF	8.2pF	0R	NF	
32K-less	10K	NF	NF	NF	NF	NF	0R	

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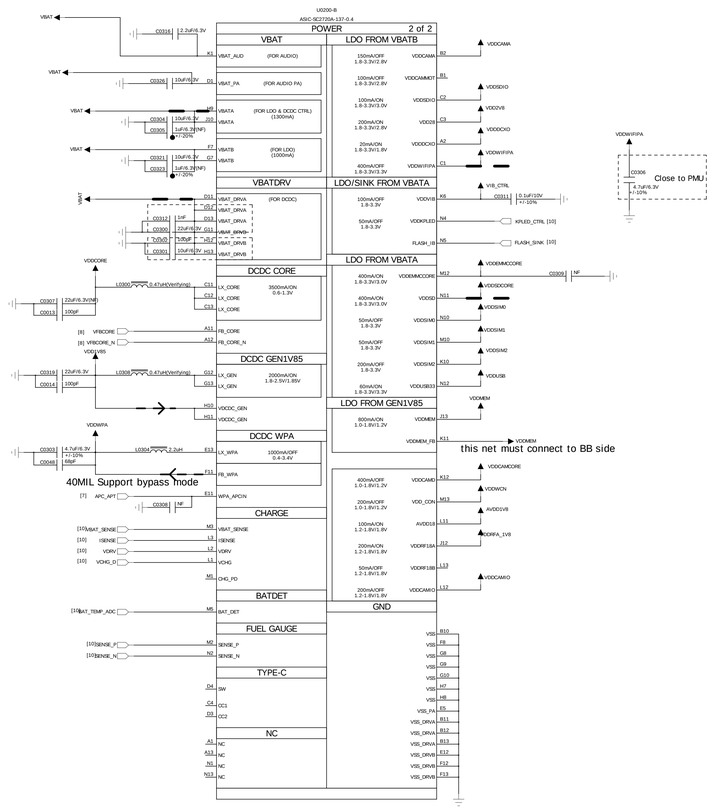
PMU_POWER



SC2720 DCDC output

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
VBAT	DCDCCORE	0.4-1.4	3500mA	0.9V	ON
	DCDCGEN1V8	1.6-2.1	2000mA	1.85V	ON
	DCDCWPA	0.4-3.4 2x and 2.5x ramp	1000mA		OFF

SC2720 LDO output

Input Power	Power Name	Output Voltage (V)	Output Current	Default Voltage	Default
LDO from VBAT	VDDCAMA	1.8-3.3	150mA	2.8V	OFF
	VDDCAMMOT	1.8-3.3	100mA	2.8V	OFF
	VDDSIM0	1.8-3.3	50mA		OFF
	VDDSIM1	1.8-3.3	50mA		OFF
	VDDSIM2	1.8-3.3	200mA		OFF
	VDDEMMCCORE	1.8-3.3	400mA	3.0V	ON
	VDDSD	1.8-3.3	400mA	3.0V	ON
	VDDSDIO	1.8-3.3	400mA	3.0V	ON
	VDD28	1.8-3.3	100mA	2.8V	ON
	VBATBK	1.8-3.3	10mA	3.0V	ON
	VDDVIB	1.8-3.3	100mA		OFF
	VDDKPLED	1.8-3.3	50mA		OFF
	VDDWIFIPA	1.8-3.3	400mA	3.3V	OFF
	VDDDCXO	1.8-3.3	20mA	1.8V	ON
	VDDUSB33	1.8-3.3	60mA	3.3V	ON
LDO from DCDCGEN	VDD_MEM	1.0-1.8	800mA	1.2V	ON
	VDDCAMD	1.0-1.8	400mA	1.25V	OFF
	VDDRFB	1.2-1.8	50mA	1.8V	OFF
	VDD_CON	1.0-1.8	200mA	1.2V	OFF
	VDDCAMIO	1.2-1.8	200mA	1.8V	OFF
	AVDD18	1.2-1.8	100mA	1.8V	ON
	VDDRFA	1.2-1.8	200mA	1.8V	ON

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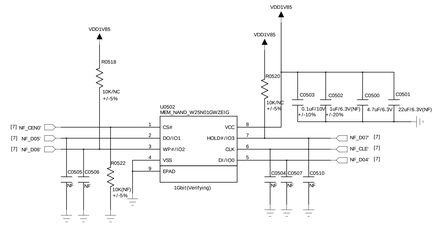
SPI FLASH(Default)

SPI_NAND_D0
SPI_NAND_D1
SPI_NAND_D2
SPI_NAND_D3

CO-PAD



GD5F1GQ4RE9IGR AND W25N01GWZEIG

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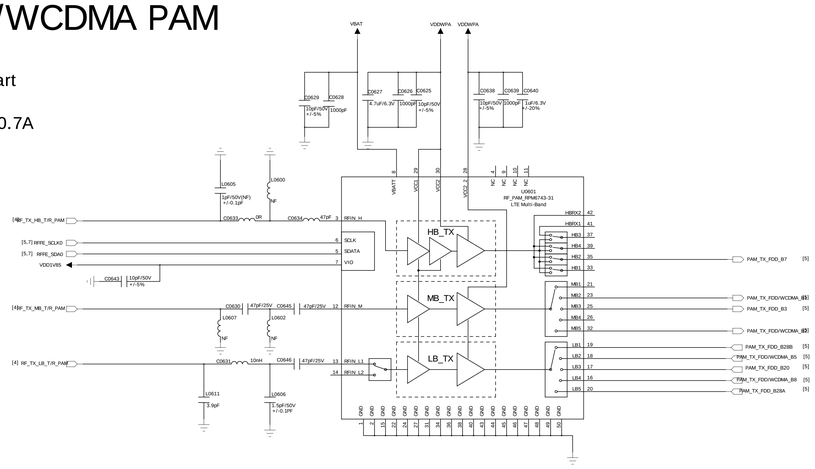
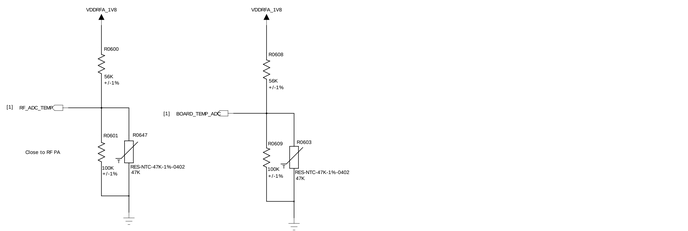
LTE/WCDMA PAM

PA Part

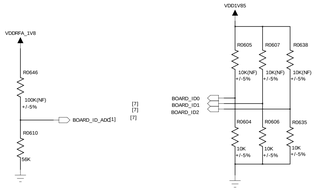
Ipp=0.7A



NTC

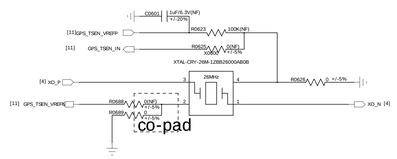


Band info



DCXO default for SC2332B

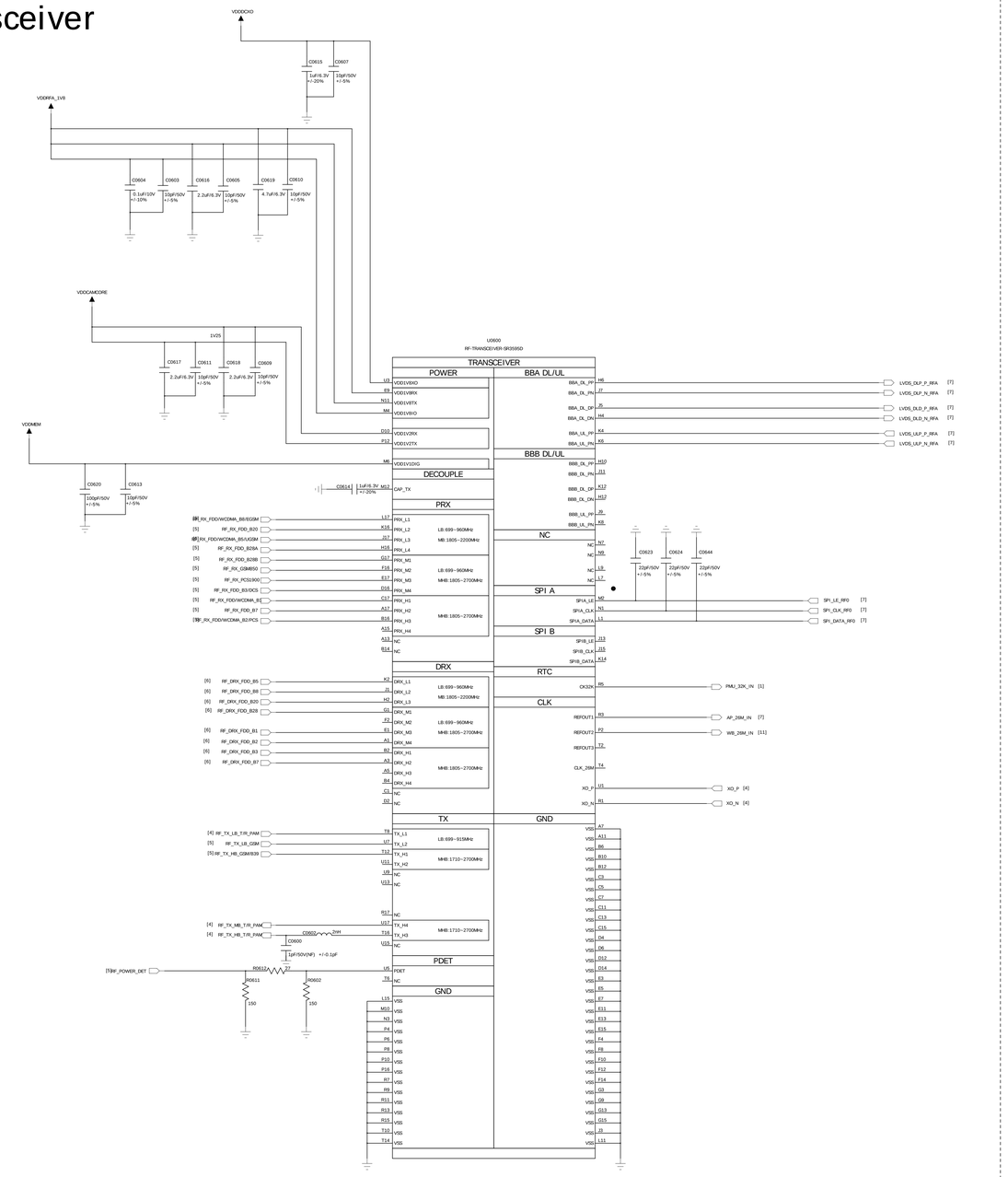
TSX backup for SC2342B



SC2332B&DCXO VS SC2342B&TSX and BoardID0

Type	X0600	R0623	R0625	R0626	R0688	R0689	R0605	R0604
DCXO Solution for SC2332B	DCXO	NF	NF	0R	NF	0R	NF	10K
TSX Solution for SC2342B	TSX	100K	0R	NF	0R	NF	10K	NF

Transceiver



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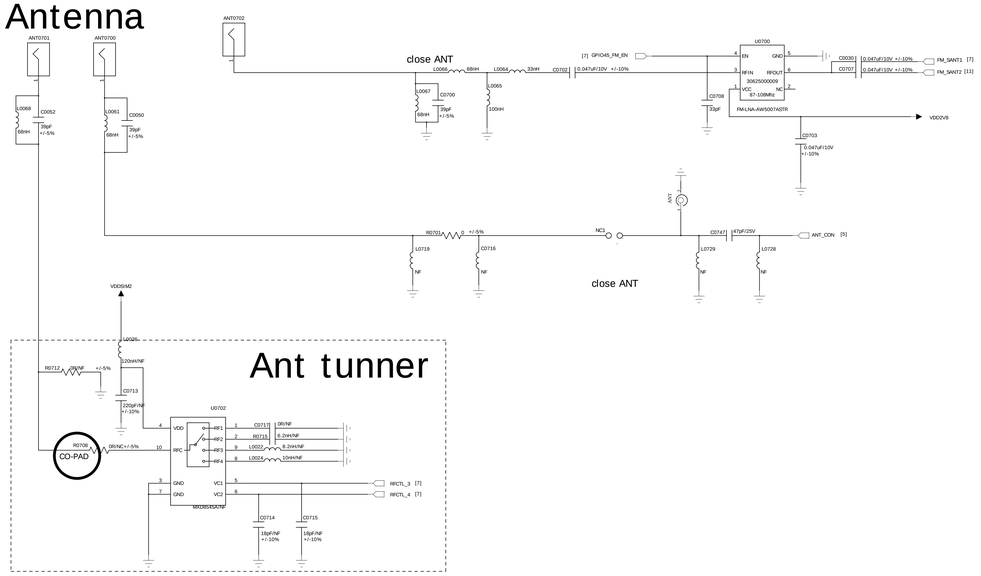
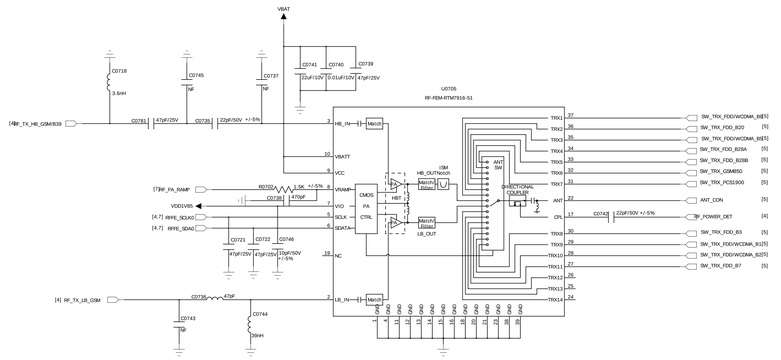
Primary Antenna

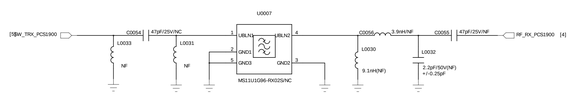


Ant tunner

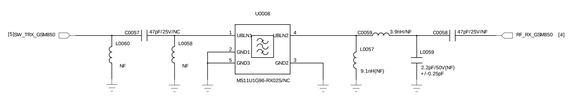
TDD B34/B39/GSM PA + Switch



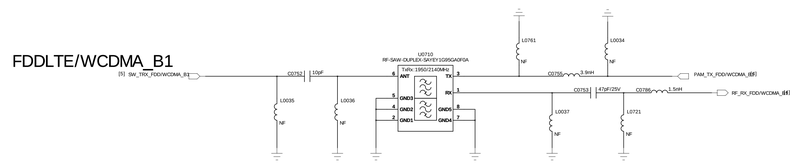
PCS1900



GSM850



FDDLTE/WCDMA_B1



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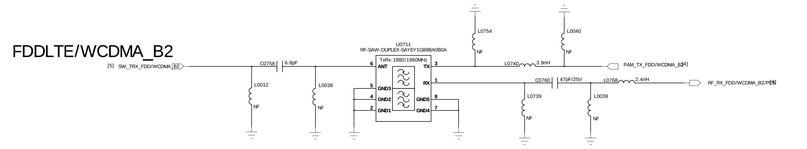
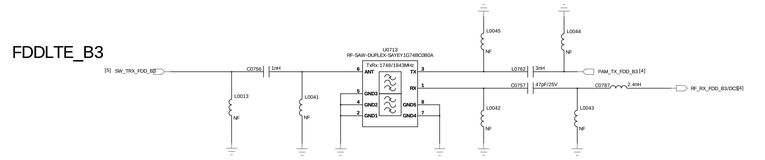
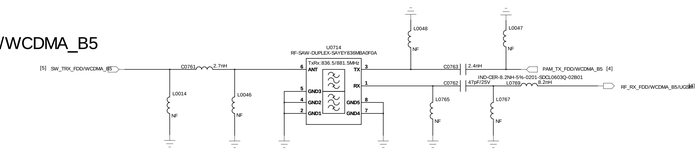


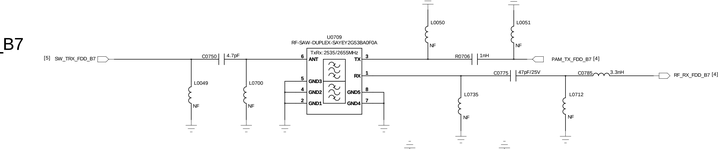
FDDLTE_B3



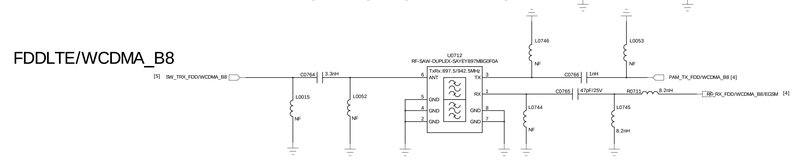
FDDLTE/WCDMA_B5



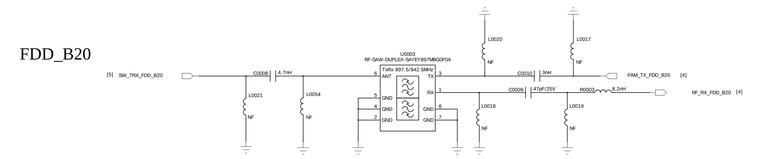
FDDLTE_B7



FDDLTE/WCDMA_B8



FDD_B20



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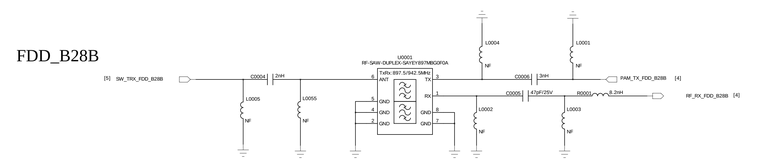
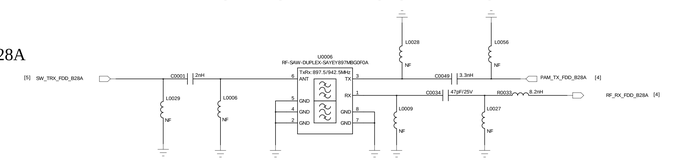


FDD_B28A



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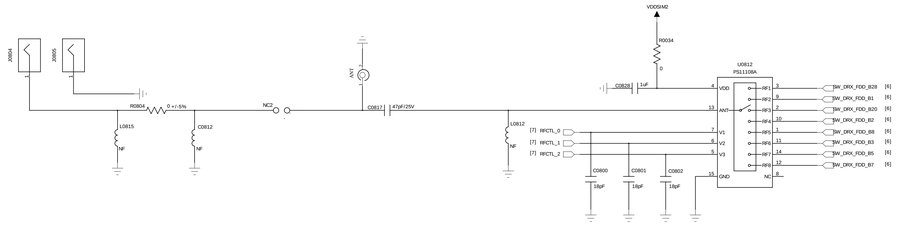
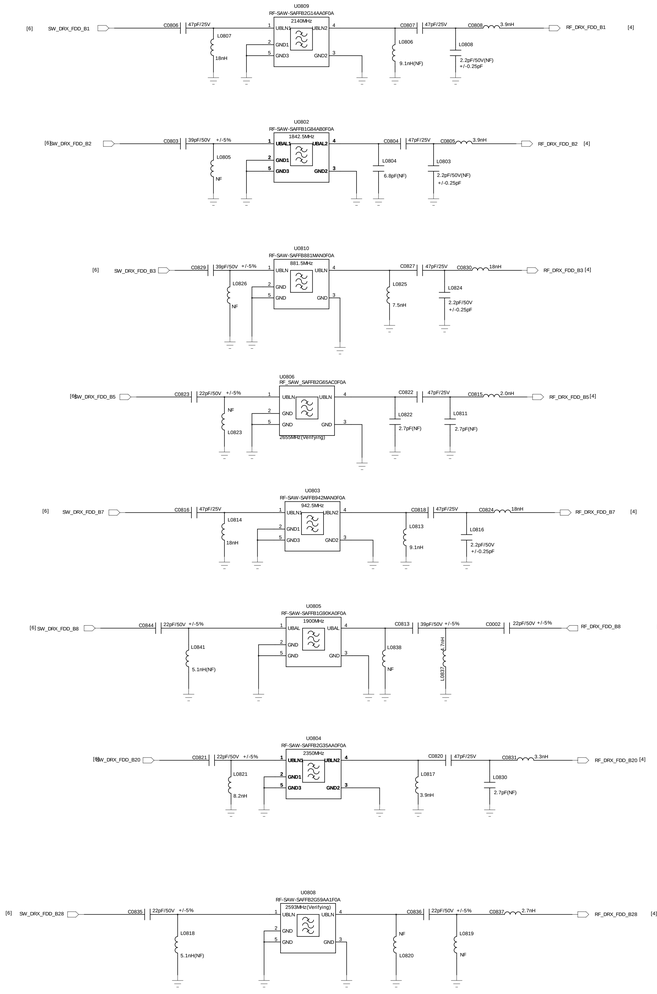
Diversity LTE Path





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DIGITAL

U2100-A
BB_LM89117_BGA_311_P040

DIGITAL 1 of 4

RFSPI (VIO1V8)

[4] SPI_CLK_RFG	C4	RFSCK
[4] SPI_DATA_RFG	C2	RFSDA
[4] SPI_LE_RFG	C3	RFSEN (H)

RFCTRL (VIO1V8)

FOR LTE/WCDMA/GSM

[6] RFCTL_0	L4	RFCTL0/GPIO4
[6] RFCTL_1	N4	RFCTL1/GPIO5
[6] RFCTL_2	M4	RFCTL2/GPIO6
[6] RFCTL_3	N2	RFCTL3/GPIO7
[6] RFCTL_4	P3	RFCTL4/GPIO8
	N1	RFCTL5/GPIO9
[12]GPIO10_WIB_SW	P2	RFCTL6/GPIO10
[12]GPIO11_GPS_OE	R1	RFCTL7/GPIO11
[11] WCN_TXS_EN	R3	RFCTL8/GPIO12
[11] WB_TX_REQ	R2	RFCTL9/GPIO13
[11] WB_CHIP_EN	T2	RFCTL10/GPIO14
[12] GPS_3.3V_EN	P4	RFCTL11/EXTINT2/GPIO15

EXTINT2

FOR LTE

EXTINT3

GPIO19_REC_EN	V2	RFCTL16/EXTINT3/GPIO19
[12] GPIO20_W_EN	V1	RFCTL17/EXTINT4/GPIO20
[11] WB_WCI_2_RXD	U3	RFCTL19/U2TXD/GPIO22
[11]WB_WCI_2_TXD	U4	RFCTL20/U2RXD/GPIO23

GPIO (VIO1V8)

[10] GPIO16/SIM_DET0	T3	GPIO16/SIM_DET0
[11] WB_RST	R4	GPIO17/PWMA/XEYIN5
[11] WB_INT	U2	GPIO18/PWMB/XEYOUT5
	T4	GPIO21/SIM_DET1

RFFE (VIO1V8)

[4,5] RFFE_SCLK0	M2	RFFE_SCK
[4,5] RFFE_SDA0	M1	RFFE_SDA

EXTINT (VIO1V8)

[5]GPIO45_FM_EN	Y3	EXTINT0/EXT_XTL_EN0/GPIO45
[10] EINT1_SOS	U8	EXTINT1/CMDCLK1/CLK_AUX1/GPIO46

IIC1 (VIO1V8)

	W8	SCL1/GPIO47
	W9	SDA1/GPIO48

DET (VIO1V8)

[5]GPIO49_AW9010_EN	U9	TF_DET/GPIO49
[1] BUA_BAT_DET	V10	BUA_DET/GPIO50

EXT_XTL_EN1

LCM CTRL (VIO1V8)

LCM reset output

[10] LCM_RSTN	U6	LCM_RSTN/GPIO38

LCM frame mark input

[10] LCM_FMARK	U7	LCM_FMARK/GPIO39

SPI LCD (VIO1V8)

SPI data and command output

[10] LCM_SPI_RS	Y7	SPI0_CS1/LCM_CD/GPIO44
[10] LCM_SPI_CSN	W6	SPI0_CS0/LCM_CS/GPIO40
[10] LCM_SPI_DI	V7	SPI0_DO/LCMD5G1/GPIO41
	W7	SPI0_DI/LCMD6G1/GPIO42

SPI data dock output

[10] LCM_SPI_CLK	V8	SPI0_CLK/LCMD7G1/GPIO43

CCIR/MCU LCD (VIO1V8)

[10] CAM_SPI_DI0_CCIR_D00	B6	CCIRD0/LCMD0G0/SDI_0G1/GPIO105
[10] CAM_SPI_DI1_CCIR_D01	A6	CCIRD1/LCMD1G0/SDI_1G1/GPIO106
[10] CAM_SPI_DI2_CCIR_D02	B8	CCIRD2/LCMD2G0/SDI_2G1/GPIO107
[10] CAM_SPI_DI3_CCIR_D03	A5	CCIRD3/LCMD3G0/SDI_3G1/GPIO108
[10] CCIR_D04	E9	CCIRD4/LCMD4G0/GPIO109
[10] CCIR_D05	E7	CCIRD5/LCMD5G0/GPIO110
[10] CCIR_D06	E8	CCIRD6/LCMD6G0/GPIO111
[10] CCIR_D07	B5	CCIRD7/LCMD7G0/GPIO112
[10] CCIR_HS	D7	CCIRHS/LCMD0G0/GPIO104
[10] CAM_SPI_SSN_CCIR_VS	B7	CCIRVS/LCMWRG0/SSNG1/GPIO103

SDI_0_CCIR_00
SDI_1_CCIR_01
SDI_2_CCIR_02
SDI_3_CCIR_03
CCIR_04
CCIR_05
CCIR_06
CCIR_07
CCIRHS
SSN_CCIRVS

CAM CTRL (VIO1V8)

[10] CAM_MCLK0	A9	CMMCLK/CLK_AUX2/DTDI/GPIO97
[10]CAM_SPI_CLK_CCIR_PCLK	A8	CMPCLK0/SDIG1/GPIO98
[10] CAM_PWDN0	C9	CMPDI/GPIO100
[10] CAM_RST0	D9	CMRST/GPIO101

C2101 10pF/50V +/-5%
C2102 NF
R2132 51

[10] EINT5	C8	EXTINT5/LCMRDG1/GPIO113
[10] EINT6	D6	EXTINT6/GPIO114 (H)
[15] I2C0_SCL	B10	SCL0/GPIO96
[15] I2C0_SDA	B9	SDA0/GPIO95

WPU

[4] BOARD_ID2	A3	SDIG0/LCMWRG1/ISZLRCK/GPIO115 (H5)
[4] BOARD_ID1	A2	SCKG0/LCMD2G1/I1SZCLK/GPIO118
[4] BOARD_ID0	B2	SDI_0G0/LCMD0G1/I1SZDI/GPIO116
		SDI_1G0/LCMD1G1/I1SZDI/GPIO117
		SDI_2G0/LCMD3G1/GPIO119
		SDI_3G0/LCMD4G1/GPIO0

GPIO117
GPIO119
GPIO0

KEYPAD (VIO1V8)

	W2	KEYOUT0/GPIO28
	V3	KEYOUT1/GPIO29
	V4	KEYOUT2/GPIO30
	U5	KEYOUT3/GPIO31
	W3	KEYOUT4/GPIO32
	V5	KEYIN0
	W4	KEYIN1
	Y4	KEYIN2
	W5	KEYIN3/SCL2/GPIO36
	Y5	KEYIN4/SDA2/GPIO37

KEYOUT0 [10]
KEYOUT1 [10]
KEYOUT2 [10]
KEYOUT3 [10]
KEYOUT4 [10]
KEYIN0 [10]
KEYIN1 [10]
KEYIN2 [10]
KEYIN3 [10]
KEYIN4 [10]

SDIO 0 (VSD0)

Close to BB

	W18	SD0_CLK/GPIO56
	W16	SD0_CMD/GPIO53
	W18	SD0_DI/GPIO54
	W17	SD0_D1/GPIO55
	Y17	SD0_D2/GPIO52
	Y16	SD0_D3/GPIO51

W18 33R +/-5% 7F_SD0_CLK [10]
7F_SD0_CMD [10]
7F_SD0_D0 [10]
7F_SD0_D1 [10]
7F_SD0_D2 [10]
7F_SD0_D3 [10]

SDIO 2 (VIO1V8)

C2106 10pF/50V(NP)

	R11	SD2_CLK/SPI1_CLK/GPIO91
	D12	SD2_CMD/SPI1_DI/GPIO92
	E12	SD2_D0/SPI1_DO/GPIO93
	D10	SD2_D1/GPIO94
	E10	SD2_D2/GPIO95
	E11	SD2_D3/SPI1_CSN/GPIO96

R11-R2117 0

WCN_SD_CLK [11]
WCN_SD_CMD [11]
WCN_SD_D0 [11]
WCN_SD_D1 [11]
WCN_SD_D2 [11]
WCN_SD_D3 [11]

SIM 0 (VSIM0)

	W19	SIM0_CLK
	Y19	SIM0_DA
	W20	SIM0_RST

SIM0_CLK [10]
SIM0_DA [10]
SIM0_RST [10]

SIM 1 (VSIM1)

	U17	SIM1_CLK
	V18	SIM1_DA
	V17	SIM1_RST

SIM1_CLK [10]
SIM1_DA [10]
SIM1_RST [10]

IIS 1 (VIO1V8) TP2100/TP2101/TP2102 close to BB ball

	A12	I1SDI/KEYIN6/GPIO87
	B12	I1SDO/KEYOUT6/GPIO88
	C12	I1SCLK/KEYOUT7/GPIO89
	A13	I1SLRCK/KEYIN7/EXT_XTL_EN1/GPIO90

BT_PCM_OUT [11]
BT_PCM_IN [11]
BT_PCM_CLK [11]
BT_PCM_SYNC [11]

UART 0 (VIO1V8)

	C13	U0TXD/EXT_XTL_EN2/GPIO80
	D13	U0RXD/EXT_XTL_EN3/GPIO81
	B15	U0CTS/PWMC/GPIO82
	A15	U0RTS/EXTINT7/GPIO83

R2104 22R
R2105 22R

U0TXD [11,12]
U0RXD [11,12]
GPS_U0CTS [11]
GPS_U0RTS [11]

placed closed

UART 1 (VIO1V8)

	B14	U1TXD/GPIO84
	A14	U1RXD/GPIO85

BB_U1TXD [10]
BB_U1RXD [10]

ROM Code log from U1ART

NAND (VIO1V8)

	K20	NF_DATA_0/GPIO77
	K19	NF_DATA_1/GPIO78
	L20	NF_DATA_2/GPIO76
	L17	NF_DATA_3/GPIO74
	P20	NF_DATA_4/SPI_D0/GPIO67
	N19	NF_DATA_5/SPI_D1/GPIO68
	M19	NF_DATA_6/SPI_D2/GPIO70
	N20	NF_DATA_7/SPI_D3/GPIO69
	N18	NF_DQS/GPIO71

NF_D04 [3]
NF_D05 [3]
NF_D06 [3]
NF_D07 [3]

	P18	NF_ALE/GPIO66
	L19	NF_CEN0/SPI_CS0/GPIO79
	M16	NF_CEN1/SPI_CS1/GPIO73
	P17	NF_CLE/SPI_CLK/GPIO65
	P19	NF_RBN/GPIO64
	P16	NF_RE_T/GPIO75
	M17	NF_WEN_T/GPIO72
	R17	NF_WPN/GPIO63

NF_CEN0 [3]
NF_CLE [3]

NAND address latch enable

NAND command latch enable

ARM JTAG (VIO1V8)

	W1	TCK_ARM/GPIO26
	Y2	TMS_ARM/GPIO27

MTCK [7]
MTMS [7]

TP3 TP4
[7]MTCK
[7]MTMS

SYSTEM (VIO1V8)

	B13	CLK_AUX0/GPIO86

CON_32K_IN [11,12]

CON_32K_IN need protect

SDIO0 support SDIO3.0 4bit 208MHz with 1V8 and 3V

SDIO2 support SDIO3.0 4bit 208MHz with 1V8 only

SC2332B&debugbus VS SC2342B&UART0

Type	R2156	R2140	R2157	R2141	R2159	R2158	R2160	R2161
DCXO Solution for SC2332B	NF	0R	NF	0R	NF	0R	NF	0R
TSX Solution for SC2342B	0R	NF	0R	NF	0R	NF	0R	NF

BB strapping pin

Ball name	Signal name in boot code	Value	Descriptions	Default vale
U1TXD	USB_DLOAD_EN	0	USB download(no time out)	1(ie=1,wpu)
		1	UART download	
AUD_DAD1	ARM_BOOT_MD0	00	**00: Reserved**	11(MSB:md1, LSB:md0)
AUD_DAD0		01	**01: SD boot(master, no time out)**	
	ARM_BOOT_MD1	10	**10: SLC SPI NAND Flash**	
		11	**11: SLC Parallel NAND Flash**	
ADI_SCLK	ARM_BOOT_MD2	0	**USB download, if EUT get 3 "7E" from PC through USB within timeout period.**	1(ie=1,wpu)
		1	**Normal Boot Flow**	
PTEST	PTEST_FUNC_MODE	0	Normal mode	0
		1	Product Test Mode	

U2100-B
BB_LM89117_BGA_311_P040

ADI synchronous clock

RF / ANALOG 2 of 4

A to D I/F (VIO1V8)

[1] ADI_SCLK	U11	ADI_SCLK
[1] ADI_D	Y10	ADI_D
[11]AUD_SCLK	Y13	AUD_SCLK
[11]AUD_ADSYNC	W11	AUD_ADSYNC
[1] AUD_ADD0	W13	AUD_ADD0
[11]AUD_DASYNC	Y11	AUD_DASYNC
[1] AUD_DAD0	Y13	AUD_DAD0
[1] AUD_DAD1	U12	AUD_DAD1

R2106 NF +/-5%
R2104 4.7K

[1] XTL_BUF_EN1	W10	XTL_BUF_EN
[1] EXT_RST_B	Y14	EXT_RST_B
[1] ANA_INT	V11	ANA_INT
[1] CHIP_SLEEP	W14	CHIP_SLEEP
[1] CLK_32K	W12	CLK_32K

D to A CLK26M

[1] PMU_26M_IN	B4	CLK26M_OUT

USB 2.0

[1] BB_USB_DP	T19	USB_DP
[1]BB_USB_DM	T20	USB_DM
R2122 6.04K,1/16W,1% 0402	U19	USB_REXT

BT RF

BT_TRX_P	B19	
BT_TRX_N	B18	

FM

FM_LANT	C20	
FM_SANT	D19	
AVSS_FM	D20	R2102 0 +/-5%

LVDSRF

LVDSRF_DL_PRI_P	G2	LVDS_DLP_P_RFA [4]
LVDSRF_DL_PRI_N	G1	LVDS_DLP_N_RFA [4]
LVDSRF_DL_DIV_P	F2	LVDS_DLD_P_RFA [4]
LVDSRF_DL_DIV_N	F1	LVDS_DLD_N_RFA [4]
LVDSRF_UL_P	H2	LVDS_UAP_P_RFA [4]
LVDSRF_UL_N	J2	LVDS_UAP_N_RFA [4]
LVDSRF_REXT	E2	R2125 10K(NP) +/-1%

close to BB

close to BB

LVDSRF0 TX data negative

APCOUT

APCOUT0	D3	RF_PA_RAMP [5]
APCOUT1	D1	APC_AFT [2]

CLK TEST

CLKOUT_TEST_MFLL	U14	
VCO_MONITOR	B16	VCO_MONITOR [8]

CLK IN

CLK26M_IN	F6	R2137 0 +/-5% AP_26M_IN [4]

T2109:6G clock output

[1] 9117_BT	[12]	

C3 10pF
L10 2.7nH
L9 1pF
C41 2.7nH
R4 NF
R13 NF

C32 NF
L7 27nH
L23 27nH
C51 NF

DRAWN:	DATED:	
CHECKED:	DATED:	
QUALITY CONTROL:	DATED:	
RELEASED:	DATED:	

COMPANY: **GEEMARC TELECOM**

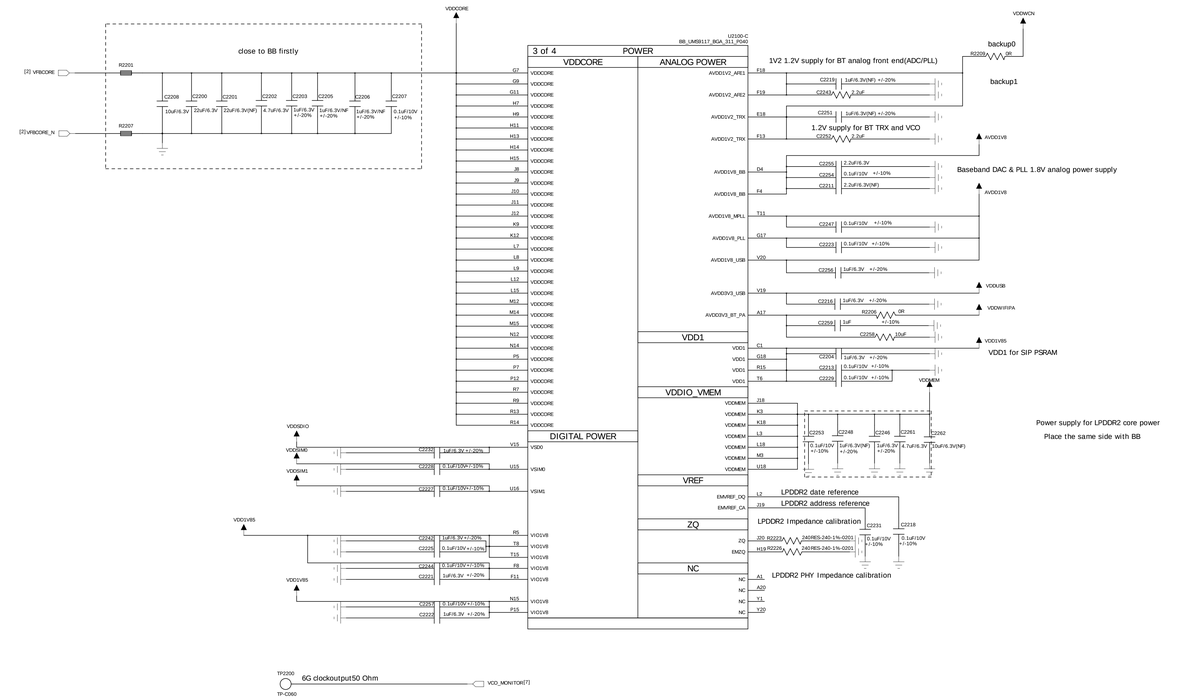
TITLE:

CODE:	SIZE:	DRAWING NO:	REV:

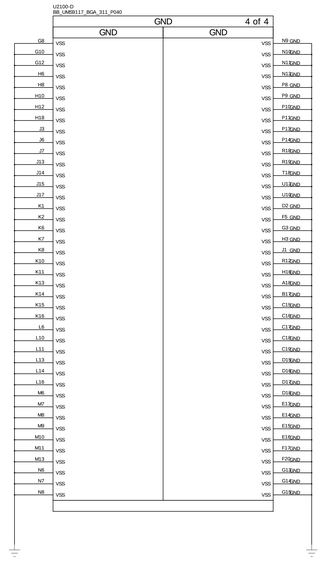
SCALE: SHEET: 7 OF 12

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

POWER



GND



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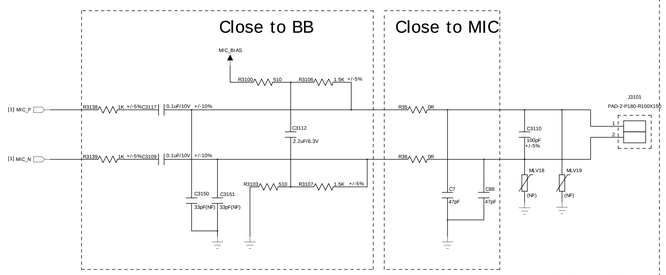
RELEASED:	DATED:	SCALE:	SHEET: 8 OF 12

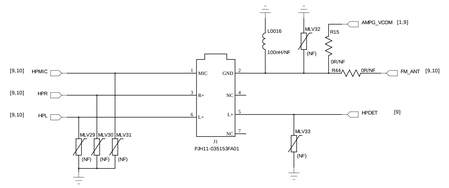
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

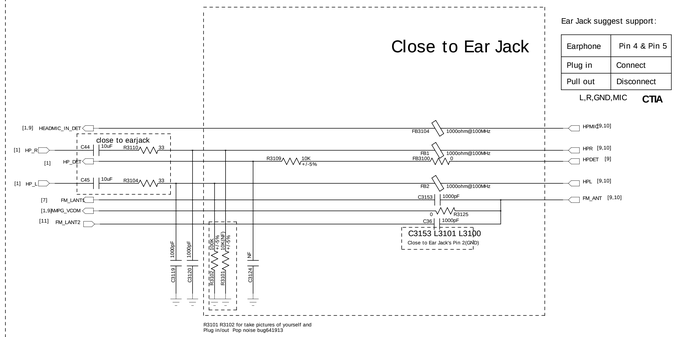
MAIN MIC



C3100,C3101 can be changed to TVS/MLV for ESD issue.

3.5mm Ear Jack





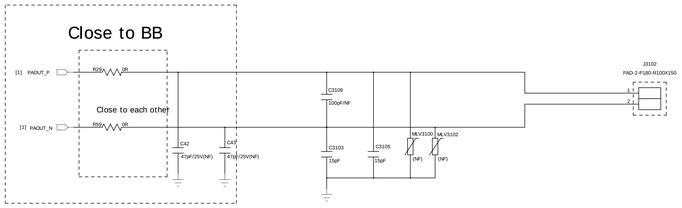
Ear Jack suggest support:

Earphone	Pin 4 & Pin 5
Plug in	Connect
Pull out	Disconnect

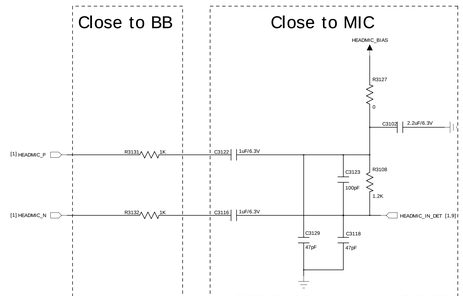
L,R,GND,MIC **CTIA**

R3101 R3102 for take pictures of yourself and Plug in/out Pop noise bug641913

SPEAKER

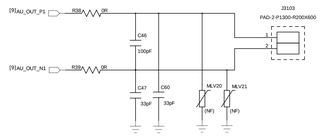


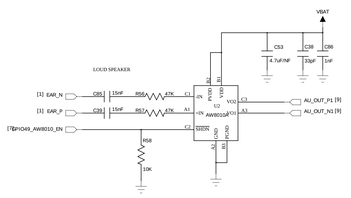
HEAD MIC



C3122&C3116 For improve headset TX T-MOS

RECEIVER





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CHECKED:	DATED:					
QUALITY CONTROL:	DATED:					
RELEASED:	DATED:	SCALE:		SHEET:	9 or 12	

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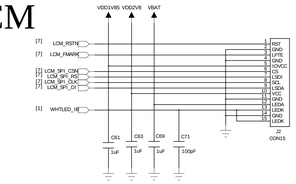
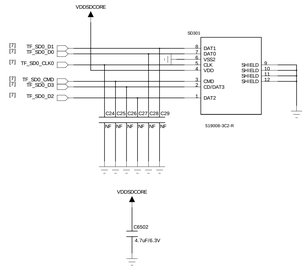
FLASH_LED

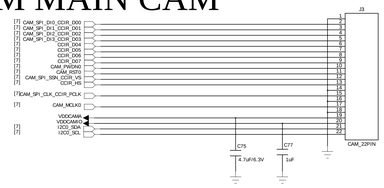
LCM

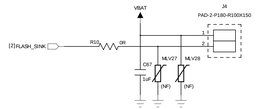


TF CARD

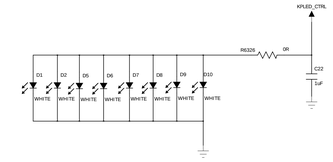


2M MAIN CAM





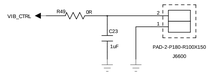
KEYPAD LED



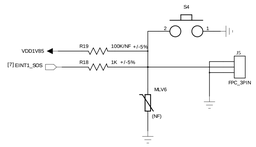
should afford 50mA current

Support 2 mode,Choose any one,and then delete 0ohm select resistor

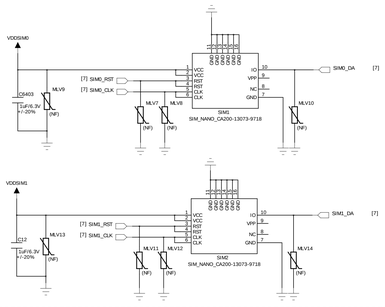
VIB



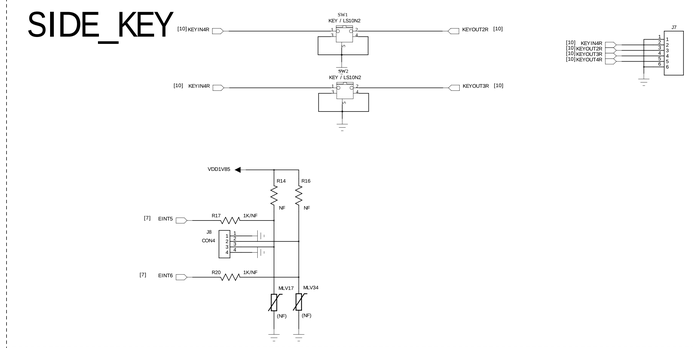
SOS



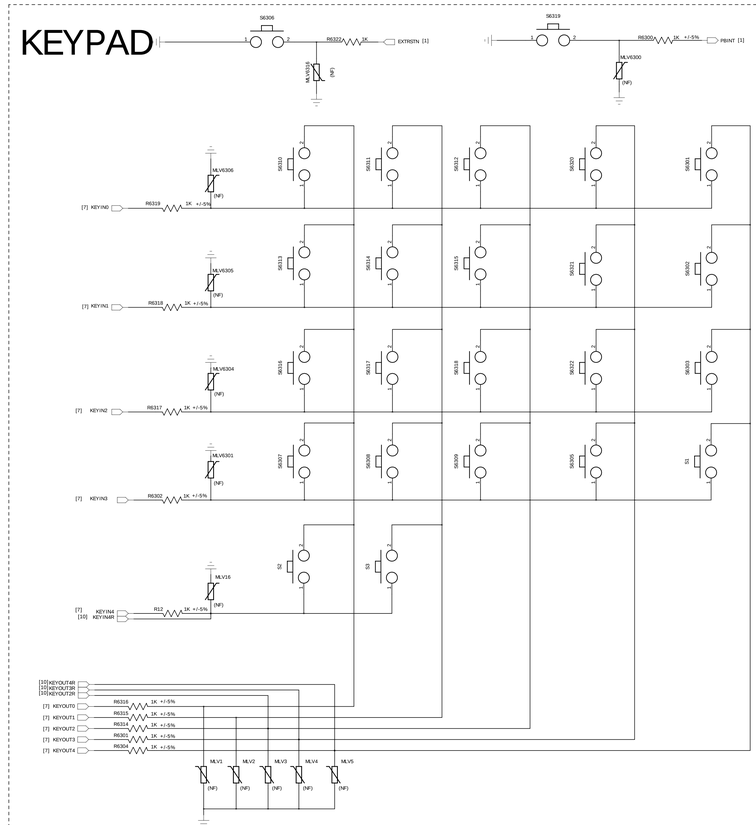
SIM0 & SIM1



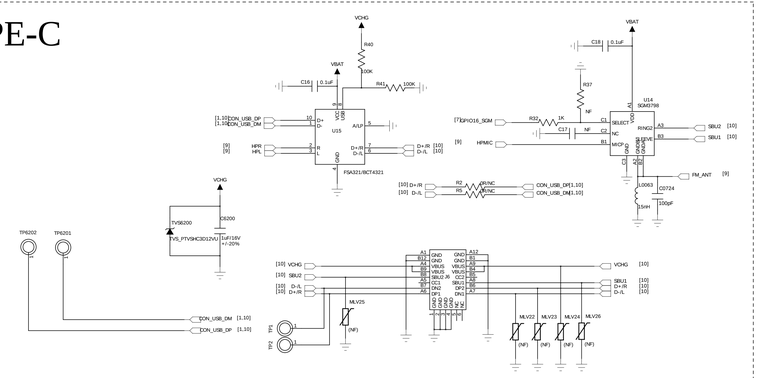
SIDE_KEY



KEYPAD

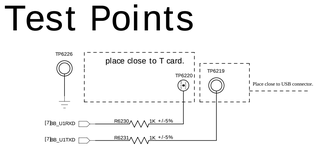


TYPE-C



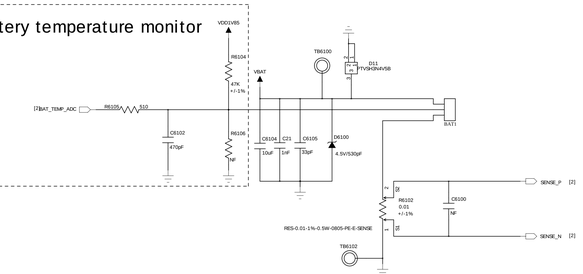
Test Points

Ball name	Signal name in boot code	Value	Descriptions	Default value
U1TXD	USB_DLOAD_EN	0	USB downloa(no time out)	1(ue1,wpu)
		1	UART download	
AUD_DAD1	ARM_BOOT_MD0	00	**00: ficare-int**	11(MSB:mdt, LSB:mdt)
		01	**01: USB boot(master, no time out)**	
AUD_DAD0	ARM_BOOT_MD1	10	**10: SLC SPI NAND Flash**	
		11	**11: SLC Parallel NAND Flash**	
ADI_SCLK	ARM_BOOT_MD2	0	**USB download, if BIT1pin 3 "TB" from PC through USB within timeout period.**	1(ue1,wpu)
		1	**Normal Boot Flow**	
IPTEST	IPTEST_FUNC_MODE	0	Normal mode	0
		1	Product Test Mode	

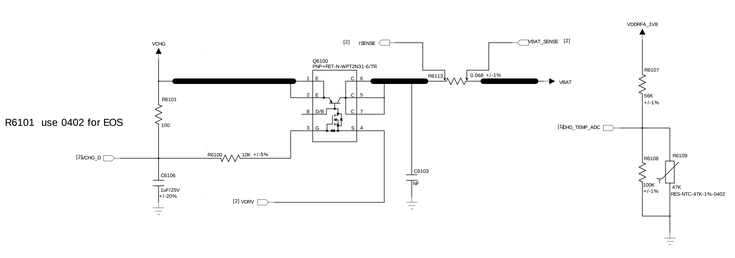


MARK POINT

Battery temperature monitor



R6101 use 0402 for EOS



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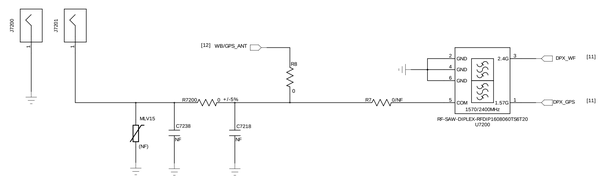
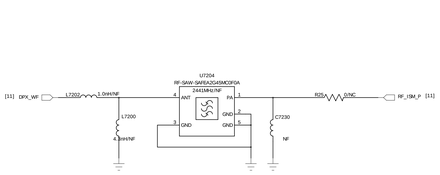
CHECKED:	DATED:				
QUALITY CONTROL:	DATED:				
RELEASED:	DATED:	SCALE:		SHEET: 10 OF 12	

REVISION RECORD			
LTR:	ECO NO:	APPROVED:	DATE:

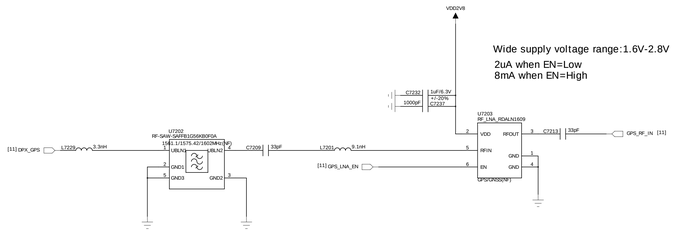
WIFI&BT&GPS ANT



WIFI/BT SAW



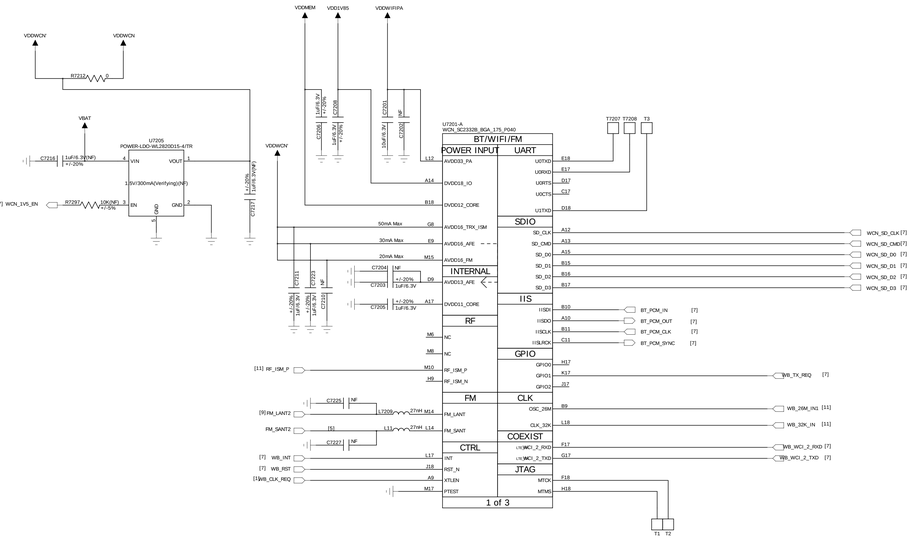
GPS SAW LNA



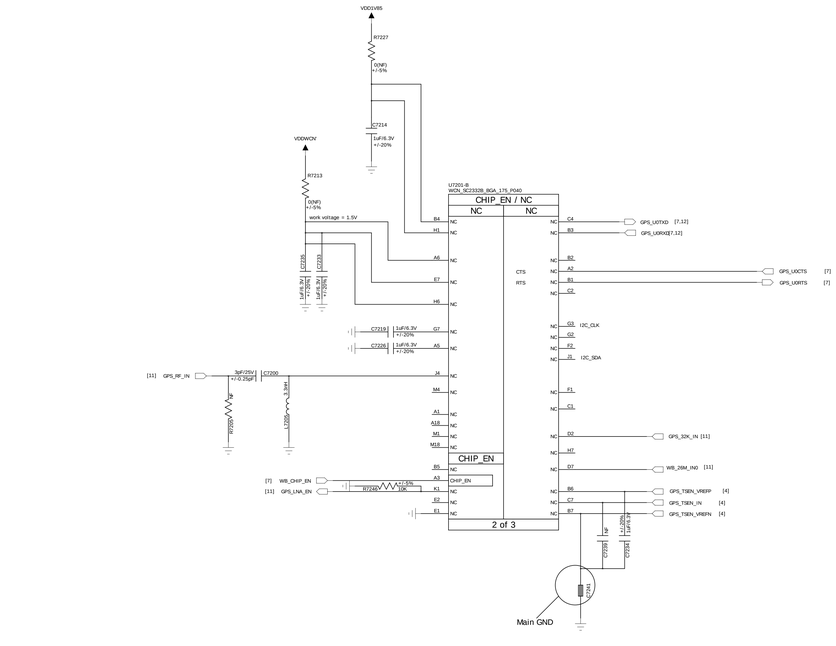
SC2332B&NA VS SC2342B&GPS

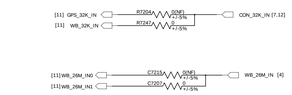
Type	U7201	R7227	R7213	R7204	R7247	FB7201	U7203	U7202
DCXO Solution for SC2332B	SC2332B	NF	NF	NF	NF	NF	NF	10K
TSX Solution for SC2342B	SC2342B	0R	0R	0R	0R	0R	SMT	SMT

Default SC2332B without GPS Backup SC2342B with GPS

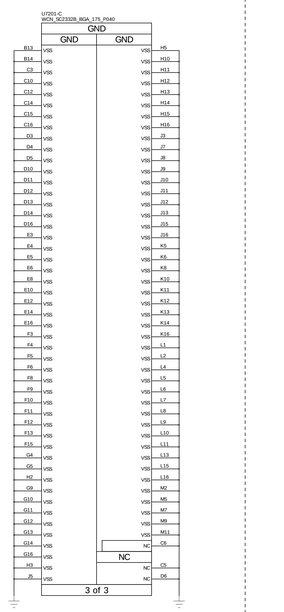


GE2 FOR BACKUP





GND



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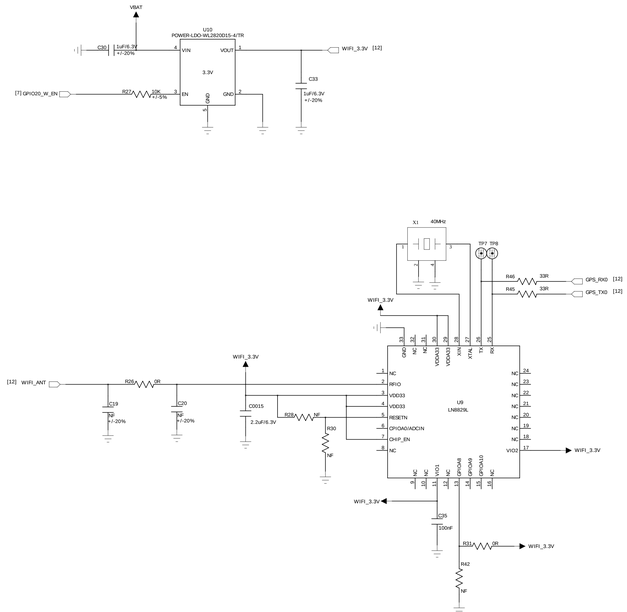
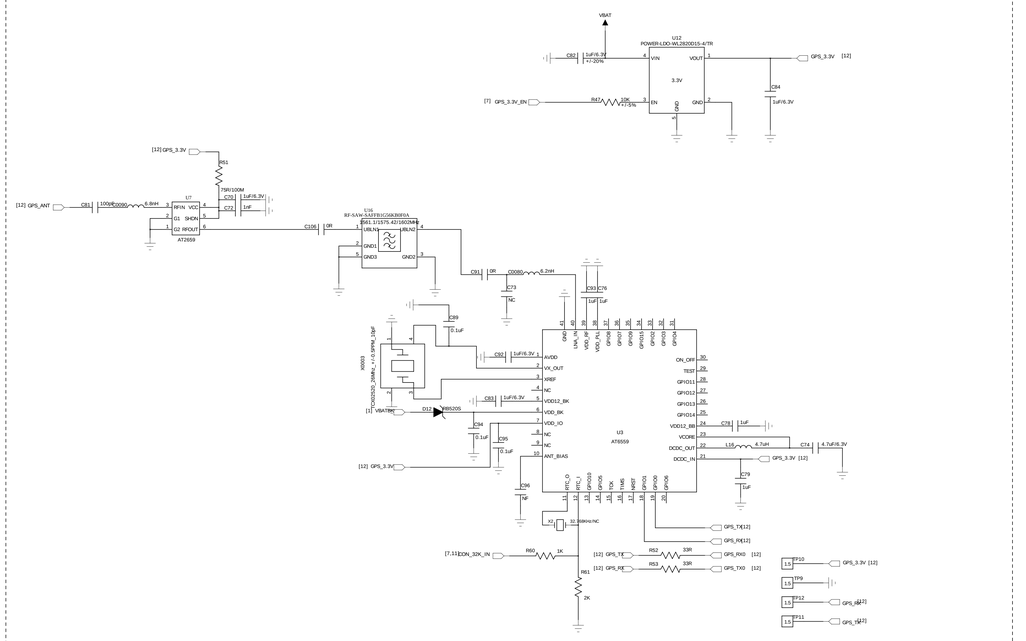
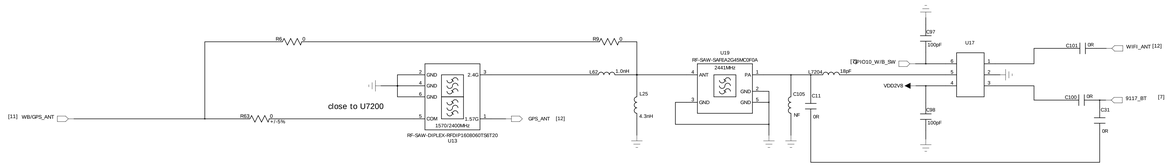
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

WIFI



GPS





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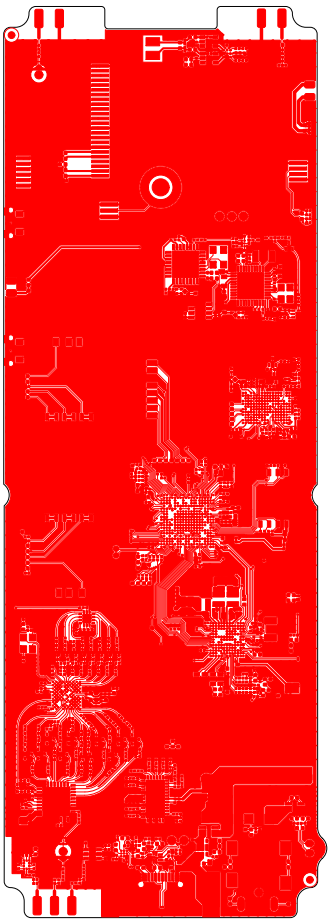
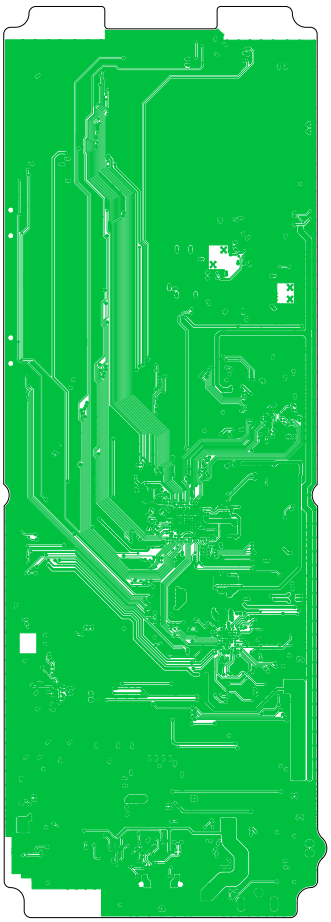
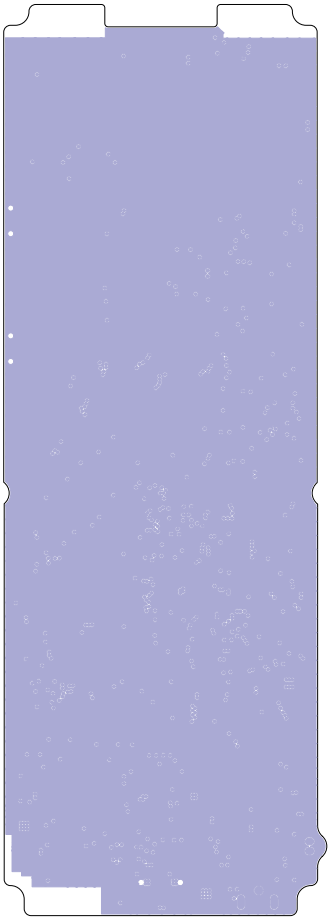
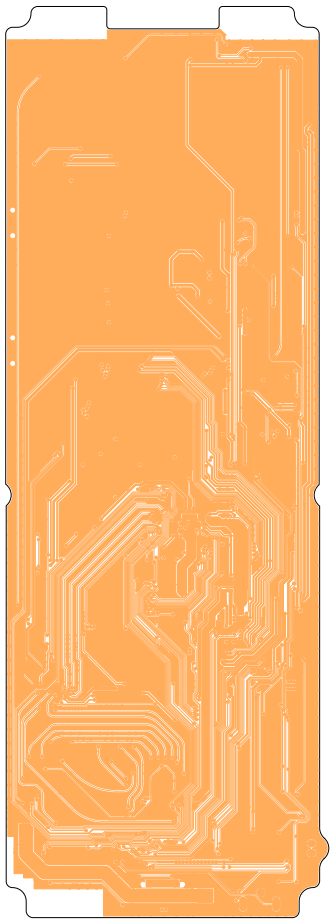
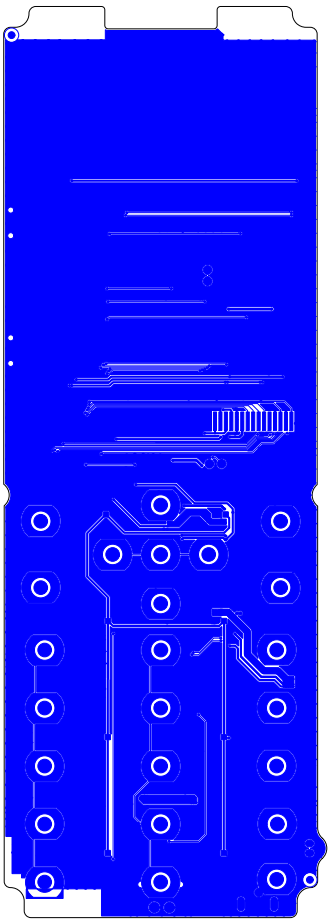
CHECKED:	DATED:	TITLE:				
QUALITY CONTROL:	DATED:	CODE:	SIZE:	DRAWING NO:		REV:
RELEASED:	DATED:	SCALE:		SHEET:	12 OF 12	











Troubleshooting

Fault analysis：

Can't turn on：

1, Upgrade the motherboard

2, Bad motherboard replacement

3, Bad battery replacement

Abnormal Display:

1, Bad display screen replacement

2, Replace the faulty FPC shaft

3, Bad motherboard replacement

No charging

1, Try with another power supply and cable.

2, Bad battery replacement

3, Bad motherboard replacement

No ringtone

1, bad speaker replacement

2, Bad motherboard replacement

No signal / Poor signal

1, Replace the Bad antenna

2, Bad motherboard replacement

Camera abnormal

1, Bad camera replacement

2, Bad motherboard replacement